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Application Number	10/692,091
Filing Date	October 23, 2003
First Named Inventor	Hung, Eric
Title	Memory Module Having Mirrored
Art Unit	2824
Examiner Name	Nguyen, Dang T.
Attorney Docket Number	03-1236

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Assignee of record of the entire interest. See 37 CFR 3.71.  Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)								
SIGNATURE of Applicant or Assignee of Record								
Signature	1-6-12 W	··· · · · · · · · · · · · · · · · · ·				Date	11JAN 06	
Name	Timothy R. Croll				Telephone	(408) 433-7472		
Title and Company Senior Corporate Counsel, IP, LSI Logic Corporation								
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.								
✓ *Total of 1	forms are subr	mitted.						

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PTO/SR/96 (12-05) Approved for use through 07/31/2006. OMB 0651-0031

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STATEMENT UNDER 37 CFR 3.73(b) Applicant/Patent Owner: LSI Logic Corporation Filed/Issue Date: October 23, 2003 Application No./Patent No./Control No.: 10/692.091 Method Module Having Mirrored Placement of DRAM Integrated Circuits Upon for a Four-Layer Printed Circuit Board LSI Logic Corporation , a Corporation (Type of Assignee: corporation, partnership, university, government agency, etc.) (Name of Assignee) states that it is: 1. the assignee of the entire right, title, and interest; or 2. an assignee of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is \_ in the patent application/patent identified above by virtue of either: A. | An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 014657 , Frame 0788 , or a true copy of the original assignment is attached. OR B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows: To: The document was recorded in the United States Patent and Trademark Office at \_\_\_, or for which a copy thereof is attached. Reel \_\_\_\_\_, Frame \_\_\_\_\_ The document was recorded in the United States Patent and Trademark Office at \_\_\_, or for which a copy thereof is attached. 3. From: The document was recorded in the United States Patent and Trademark Office at \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached. Additional documents in the chain of title are listed on a supplemental sheet. As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11. [NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.081 The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee. Signature Timothy R. Croll (408) 433-7472 Telephone Number Printed or Typed Name Senior Corporate Counsel, IP Title

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.